

# INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)  
AD-320J

Application Number  
10/824,012

Applicant(s)  
Wu et al.

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4/14/04

Group Art Unit  
2816

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

## FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Kan	Williams, et al. "Third-Order Sigma-Delta Modulator with Extended Dynamic Range", IEEE Journal of Solid-State Circuits, Vol. 29, No. 3, March 1994, pages 193 - 202
	Grilo et al., "Predictive Correlated Double Sampling Switched-Capacitor Integrators", 1998 IEEE, pages 9 - 12
Fan	Huang et al. "Reduced Nonlinear Distortion in Circuits with Correlated Double Sampling", IEEE Transactions on Circuits and Systems -II: Analog and Digital Signal Processing, Vol. 44, No. 7, July 1997, pages 593 -597
	Nagaraj, et al. "Reduction of Finite-Gain Effect In Switched Capacitor Filters", Electronics Letter, July 4, 1985, Volume 21, Number 14

EXAMINER  
WELLS

DATE CONSIDERED  
11/7/05

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